

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: Unassigned Filing Date: Herewith  
Confirmation No.: Unassigned  
First Named Inventor: Alexander Joffe  
Assignee: Applied Micro Circuits Corporation  
Examiner: Unassigned Art Unit: Unassigned  
Attorney Docket No.: M-5648-2D US

San Jose, California  
April 14, 2004

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are not enclosed as all documents were previously cited by or submitted to the Patent Office in prior applications relied upon for priority under 35 U.S.C. 120(37 C.F.R. § 1.98(d)). The prior applications are U.S. Application No. 09/055,033, filed April 3, 1998 (now Patent No. 6,330,584), and U.S. Patent Application No. 09/458,551, filed December 9, 1999.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
  2. a representation that a search has been made, other than as described above;
- or
3. an admission that the information cited herein is, or is considered to be material to patentability as defined in § 1.56(b).

**OP**

No fee is believed to be required. If a fee is required, please charge Deposit Account  
No.: 50-2257.

**EXPRESS MAIL LABEL NO.:**

**EV 302 216 612 US**

Respectfully submitted,

*Michael Shenker*

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Patent Attorney

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U.S. Department of Commerce, Patent and Trademark Office					Atty. Docket No.		Serial No.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Alexander Joffe			
					Filing Date		Group	
					Herewith		Unassigned	

  

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	2,686,844	Aug., 1954	Brewer.			
	AB	4,885,744	Dec., 1989	Lespagnol et al.			
	AC	5,058,144	Oct., 1991	Fiala et al.			
	AD	5,062,106	Oct., 1991	Yamazaki			
	AE	5,233,606	Aug., 1993	Pashan et al.			
	AF	5,261,062	Nov., 1993	Sato			
	AG	5,311,509	May., 1994	Heddes et al.			
	AH	5,337,308	Aug., 1994	Fan			
	AI	5,357,617	Oct., 1994	Davis et al.			
	AJ	5,361,337	Nov., 1994	Okin			
	AK	5,457,687	Oct., 1995	Newman			

  

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL	WO 95/20282	Jul., 1995	WO.				
	AM	WO 95/32570	Nov., 1995	WO.				
	AN	WO 98/09409	Mar., 1998	WO.				
	AO	JP 10049390	20 Feb. 1998	JP				
	AP	JP 3101551	26 Apr. 1991	JP				

  

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	AQ	"An Overview of ATM LAN Emulation", World Wide Web, Interphase Corporation, Mar. 1996, pp. 1-8.
	AR	"LAN Emulation", World Wide Web, Nov. 1995, pp. 1-18.
	AS	B. Klessig, "ATM LAN Emulation", World Wide Web, 3Com Corporation, pp. 1-20.

  

Examiner	Date Considered
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**\*EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AT	5,517,495	May., 1996	Lund et al.				
	AU	5,528,588	Jun., 1996	Bennett et al.				
	AV	5,541,912	Jul., 1996	Choudhury et al.				
	AW	5,546,390	Aug., 1996	Stone				
	AX	5,555,264	Sep., 1996	Sallberg et al.				
	AY	5,557,611	Sep., 1996	Cappellari et al.				
	AZ	5,583,863	Dec., 1996	Darr, Jr. et al.				
	BA	5,592,476	Jan., 1997	Calamvokis et al.				
	BB	5,629,928	May., 1997	Calvignac et al.				
	BC	5,633,859	May., 1997	Jain et al.				
	BD	5,633,867	May., 1997	Ben-Num et al.				
	BE	5,689,508	Nov., 1997	Lyles				
	BF	5,704,047	Dec., 1997	Schneeberger				
	BG	5,715,250	Feb., 1998	Watanabe				
	BH	5,719,853	Feb., 1998	Ikeda				
	BI	5,748,629	May., 1998	Caldara et al.				
	BJ	5,751,951	May., 1998	Osborne et al.				
	BK	5,809,024	Sep., 1998	Ferguson et al.				
	BL	5,838,968	Nov., 1998	Culbert				
	BM	5,983,004	Nov., 1999	Shaw et al.				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	BN	"ATM Internetworking", World Wide Web, Cisco Systems, Inc., 1995, pp. 1-60.						
	BO	Allyn Romanow et al. "Dynamics of TCP Traffic Over ATM Networks" IEEE Journal of Selected Areas in Communications, vol. 13, No. 4, pp 633-641, May 1995.						
	BP	Alan Demers et al. "Analysis and Simulation Of A Fair Queueing Algorithm" .COPYRGT.1989 ACM, pp. 1-12.						
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	BQ	6,018,759	Jan., 2000	Doing et al.			
	BR	6,233,590	May., 2001	Shaw et al.			
	BS	6,591,358	Jul., 2003	Jaffrey			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	BT	"ATMS2003B Switch Controller 1 "White"", MMC Networks, Dec. 1995, 32 pages.					
	BU	Beraldi, R., et al: "Selective BECN Schemes for Congestion Control of ABR Traffic in ATM LAN", 1996 IEEE Int'l Conf. on Communications (ICC), Converging Technologies for Tomorrow's Applications, Dallas, Jun. 23-27, 1996. pp. 503-507.					
	BV	Dutton, Harry, J.R. and Lenhard, Peter, "Asynchronous Transfer Mode (ATM) Technical Overview" (IBM, Prentice-Hall PTR, New Jersey, 1995), pp. 3-1 through 3-25 and 9-1 through 9-34.					
	BW	Flanagan, William A., "ATM (Asynchronous Transfer Mode) User's Guide," (Flatiron Publishing, Inc. New York, 1994), pp. 25-49.					
	BX	Hongqing Li et al. "A Simulation Study of TCP Performance in ATM Networks With ABR and UBR Services" Proceedings vol. 3, 1996 IEEE Infocom '96, Fifteenth Annual Joint Conference of the IEEE Computer and Communications Societies, Mar. 24-28, 1996, pp v-xvi and pp 1269-1276 (21 pages total)					
	BY	Hongqing, Li, et al.: "Performance of TCP Over UBR Service in ATM Networks With Per-VC Early Packet Discard Schemes" Proceedings of the 1996 IEEE 15th Annual Int' Phoenix Conf. on Computers and Communication, Mar. 27-29, 1996, pp. 350-357.					
	BZ	Ozveren, C., et al: "Reliable and Efficient Hop-By-Hop Flow Control", Computer Communications Review, vol. 24, No. 4, Oct. 1, 1994, pp. 89-100.					
	CA	Tanenbaum, Andrew S., "Computer Networks" (Prentice Hall PTR, 3rd Ed., 1996) pp. 148-150.					
	CB	Farrens M. K. et al.: "Strategies For Achieving Improved Processor Throughput", Computer Architecture News, Association for Computing Machinery, New York, US, Vol. 19, No. 3 1 May 1991 (1991-05-01), Pages 362-369.					
	CC	Lee E. A. et al.: "Pipeline Interleaved Programmable DSP's: Architecture", IEEE Transactions on Acoustics, Speech and Signal Processing, IEEE Inc. New York, US, Vol. 35 No. 9, 1 September 1987 (1987-09-01), pages 1320-1333.					
	CD	Nemirovsky M.D. et al.: "Disc; Dynamic Instruction Stream Computer", Proceedings of the Annual+A44 International Symposium on Microarchitecture, XX, XX, 1991, pages 163-171.					
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/824,816 Filing Date: April 14, 2004  
Confirmation No.: Unassigned  
First Named Inventor: Alexander Joffe  
Assignee: Applied Micro Circuits Corporation  
Examiner: Unassigned Art Unit: Unassigned  
Attorney Docket No.: M-5648-2D US

San Jose, California  
June 22, 2004

Mail Stop Amendment  
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No.: 50-2257.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 22, 2004.

Michael Shenker 6-22-04  
Attorney for Applicant(s) Date of Signature

Respectfully submitted,

*Michael Shenker*

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U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	4,797,816	Jan. 1989	Uchiyama et al.				
	AB	5,175,732	Dec. 1992	Hendel et al.				
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	"Dynamic Instruction Stream Computer" Dr. Marion Daniel Nemirovsky, Apple Computer Corporation, 1991.						
	AR							
	AS							
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